



MAIL STOP RCE  
PATENT  
8031-1028

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yoshihiro NONAKA

Conf. 5218

Application No. 10/648,256

Group 2811

Filed August 27, 2003

Examiner Ori Nadav

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING TRANSISTORS  
FORMED ON AN INSULATING SUBSTRATE

**AMENDMENT**

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 15, 2006

Sir:

In response to the Office Action mailed December 13, 2005 and the Advisory Action mailed March 29, 2006, for which the period for response has been extended two months, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.